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BIASING SCHEME FOR LOW SUPPLY HEADROOM APPLICATIONS

5 ABSTRACT OF THE DISCLOSURE

Methods and apparatus for improving the current matching within current mirror circuits in applications such as low voltage integrated circuits. Embodiments of the present invention attempt to maintain the proper current ratio between
10 reference and output supplies by adjusting the reference output of the current mirror. An existing reference voltage on the output side of the mirror can be used or a reference voltage can be created to be used for the voltage regulation of the reference side of the current mirror.

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